

an encoder for encoding the image data and the control signal output from the image processing part into a RSDS specification, a power output part for outputting a constant-voltage; and

a display module in electrical communication with the system, said display module comprising:

a control board including a power supply part for converting the constant-voltage of the power output part into a predetermined voltage level;

a gray scale generating part for generating a gray scale voltage using the predetermined voltage level of the voltage converting part;

a gate voltage generating part for generating a gate on/off voltage using the predetermined voltage level of the voltage converting part; and

a transmission line for transmitting the encoded image data and the control signal;

a first connecting member having a data driver for generating a column signal when the image data, the control signal and the gray scale voltage are applied;

a second connecting member having a scan driver for generating a scan signal when the control signal and the gate on/off voltage are applied; and

a flat panel for forming a picture using the scan signal and the column signal.

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6. A flat panel display, comprising:

a signal converting board including an analog/digital converter for converting an analog data having an analog format and for forming a picture and a control signal for the analog data into a digital data and a digital control signal;

an image processing part for deciding a timing format of the digital data and generating a control signal for the digital data;

an encoder for encoding the digital data and the digital control signal output from the image processing part into a RSDS specification; and

a display module in electrical communication with the signal converting board, said display module comprising:

a control board including a power supply part for converting a constant-voltage into a predetermined voltage level;

a gray scale generating part for generating a gray scale voltage using the predetermined voltage level of the voltage converting part;

a gate voltage generating part for generating a gate on/off voltage using the predetermined voltage level of the voltage converting part; and

a transmission line for transmitting the encoded image data and the control signal;

a first connecting member having a data driver for generating a column signal from the image data, the control signal, and the gray scale voltage;

a second connecting member having a scan driver for generating a scan signal [when] from the control signal and the gate on/off voltage; and

a flat panel for displaying an image using the scan signal and the column signal.

Please **ADD** new claims 11-18 as follows.

--11. (New) A flat panel display, comprising:

a flat panel display having a plurality of data lines and a plurality of scan lines formed in a matrix configuration;

a system including a image signal processing part, a power output part, and encoder part, wherein the image signal processing part generates a data signal and a control signal and the encoder receives the data signal and the control signal and transmits RSDS signals;

a control board including a gray scale generating part, a gate voltage generation part, power supply part and connected to the flat panel display with a plurality of connecting members, wherein the plurality of connecting members include a plurality of column driver integrated circuits for receiving RSDS signals from the encoder.

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cm+ 12. (New) The flat panel display of claim 11, wherein the flat panel display is a liquid crystal display.

13. (New) The flat panel display of claim 11, wherein the plurality of connecting members apply the RSDS signals to the corresponding column driver integrated circuits.

14. (New) The flat panel display of claim 12, wherein the plurality of column driver integrated circuits convert the RSDS signals into a TTL signal.

15. (New) The flat panel display of claim 11, wherein the TTL is converted into a column signal and output to the plurality of data lines.

16. (New) The flat panel display of claim 11, wherein the column driver integrated circuit further comprises:

a first decoder connected to a data transmission channel for receiving the RSDS signal from the encoder and converting into a first TTL signal;

a first register in electrical communication with the first decoder for temporally storing the first TTL signal;

a second decoder connected to a control signal transmission channel for receiving the RSDS signal from the encoder and converting into a second TTL signal;

a second register in electrical communication with the second decoder for temporally storing the second TTL signal, controlling the first register, and outputting control signals to a shift register for outputting a column signal.

17. (New) The flat panel display of claim 16, wherein the first register selectively outputs signals to a data latch.

18. (New) The flat panel display of claim 16, wherein the second register selectively outputs control signals to at least one of the first register, the shift register, a data latch a converter and a buffer.--